

FIGURE 1

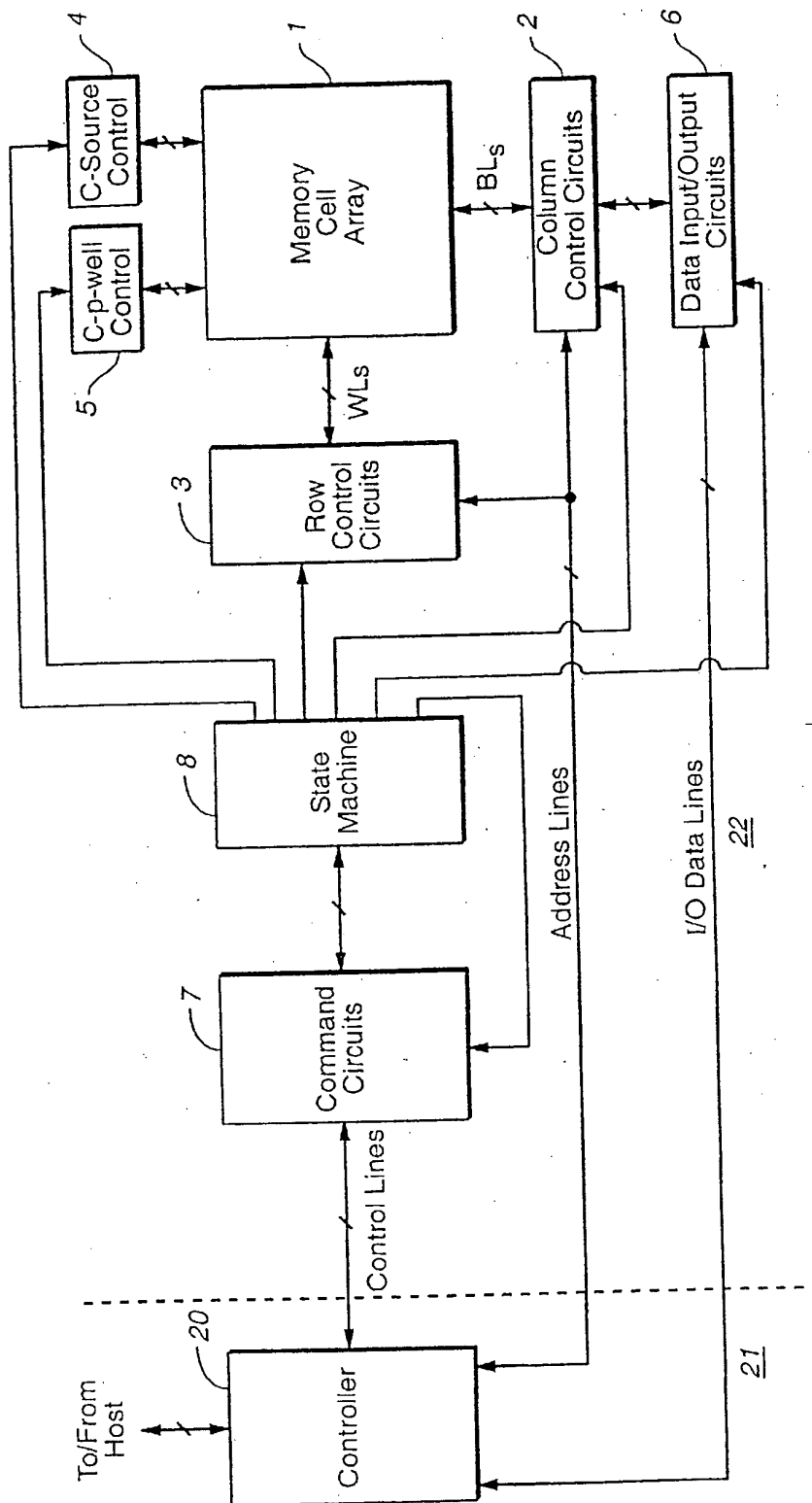
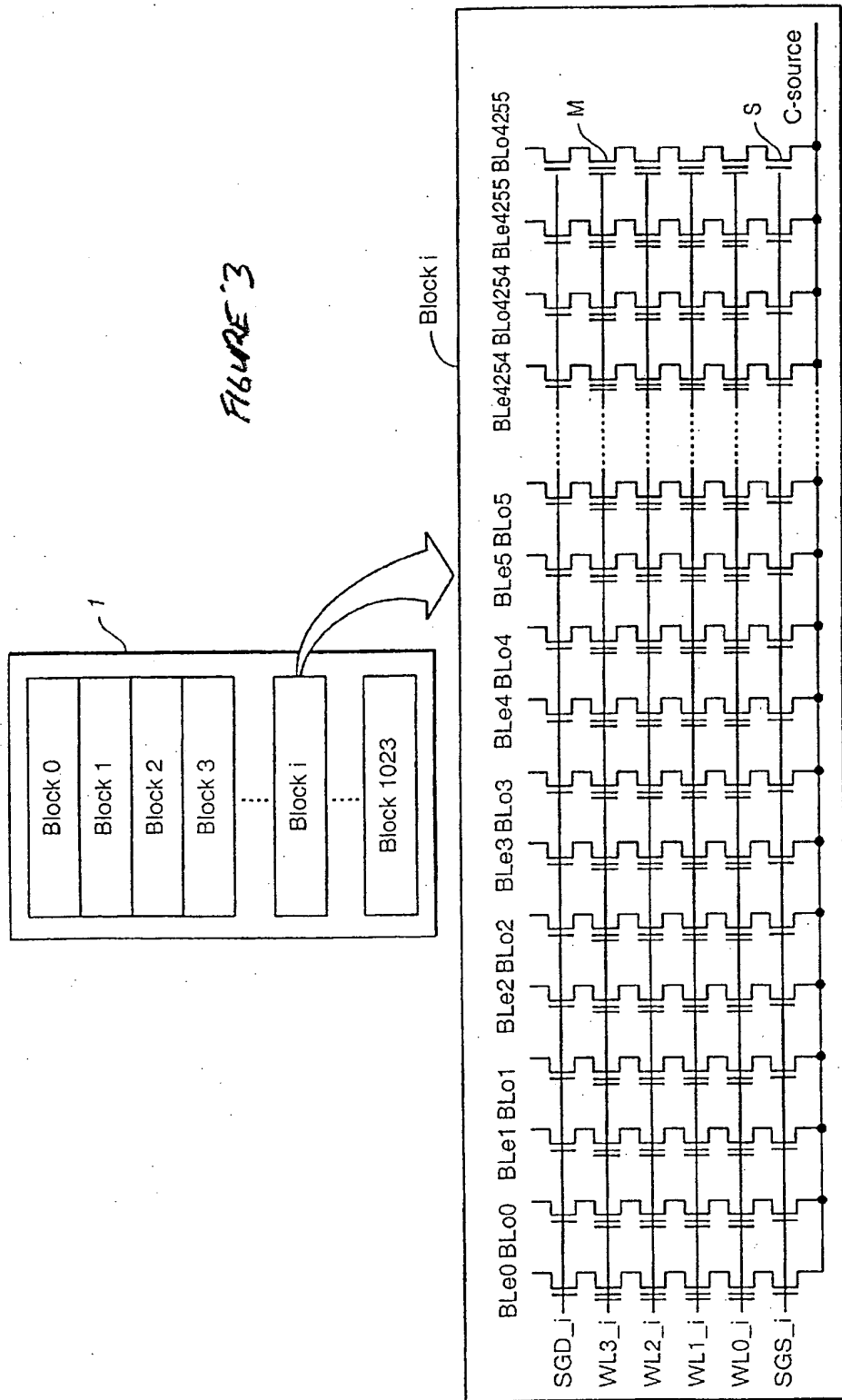


FIGURE 2

FIGURE 3



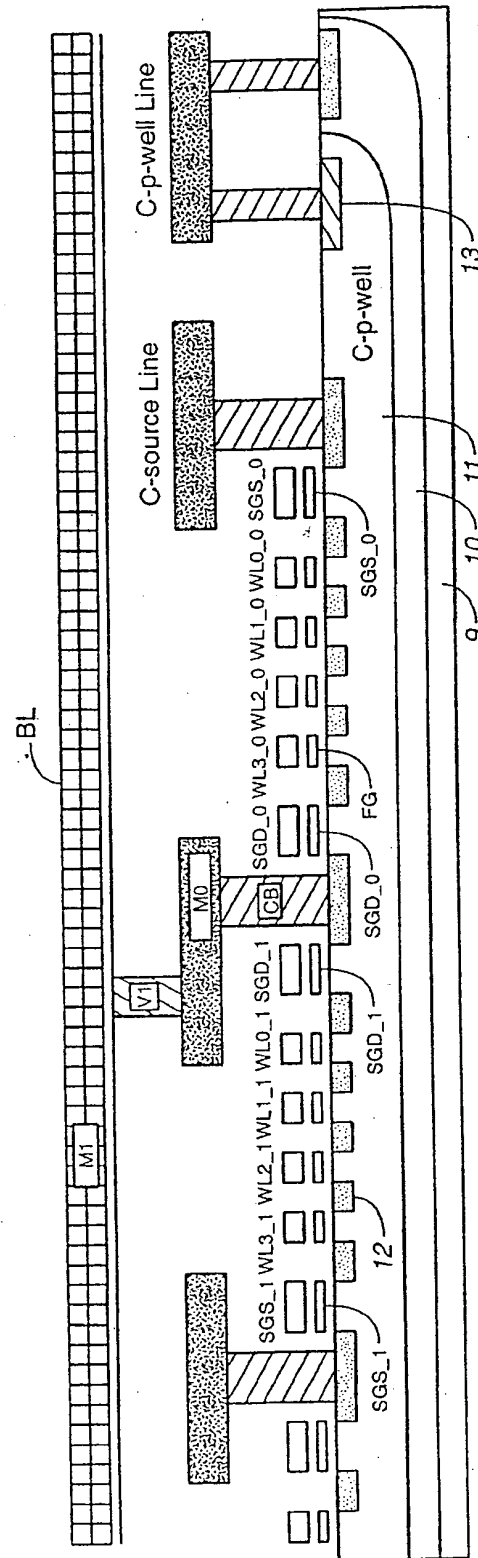


FIGURE 4

	Erase	Program Promotion	Program Inhibition	Read10	Read10 for 1st Pass	Read10 for 2nd Pass	Read00	Read00 for 2nd Pass	Read01	Verify10 for 1st Pass	Verify10 for 2nd Pass	Verify00 for 1st Pass	Verify00 for 2nd Pass	Verify01
BL <sub>e</sub>	Floating	0V	Vdd	H or L	H or L	H or L	H or L	H or L	H or L	H or L	H or L	H or L	H or L	H or L
BL <sub>o</sub>	Floating	Vdd	Vdd	0V	0V	0V	0V	0V	0V	0V	0V	0V	0V	0V
SGD	Floating	Vdd	Vdd	4.5V	4.5V	4.5V	4.5V	4.5V	4.5V	4.5V	4.5V	4.5V	4.5V	4.5V
WL3	0V	10V	10V	4.5V	4.5V	4.5V	4.5V	4.5V	4.5V	4.5V	4.5V	4.5V	4.5V	4.5V
WL2	0V	Vpgm	Vpgm	0V	0V	0V	1V	1V	2V	0.2V	0.4V	1.2V	1.4V	2.4V
WL1	0V	10V	10V	4.5V	4.5V	4.5V	4.5V	4.5V	4.5V	4.5V	4.5V	4.5V	4.5V	4.5V
WL0	0V	10V	10V	4.5V	4.5V	4.5V	4.5V	4.5V	4.5V	4.5V	4.5V	4.5V	4.5V	4.5V
SGS	Floating	0V	0V	4.5V	4.5V	4.5V	4.5V	4.5V	4.5V	4.5V	4.5V	4.5V	4.5V	4.5V
C-source	Floating	0V	0V	0V	0V	0V	0V	0V	0V	0V	0V	0V	0V	0V
C-p-well	20V	0V	0V	0V	0V	0V	0V	0V	0V	0V	0V	0V	0V	0V

FIGURE 5

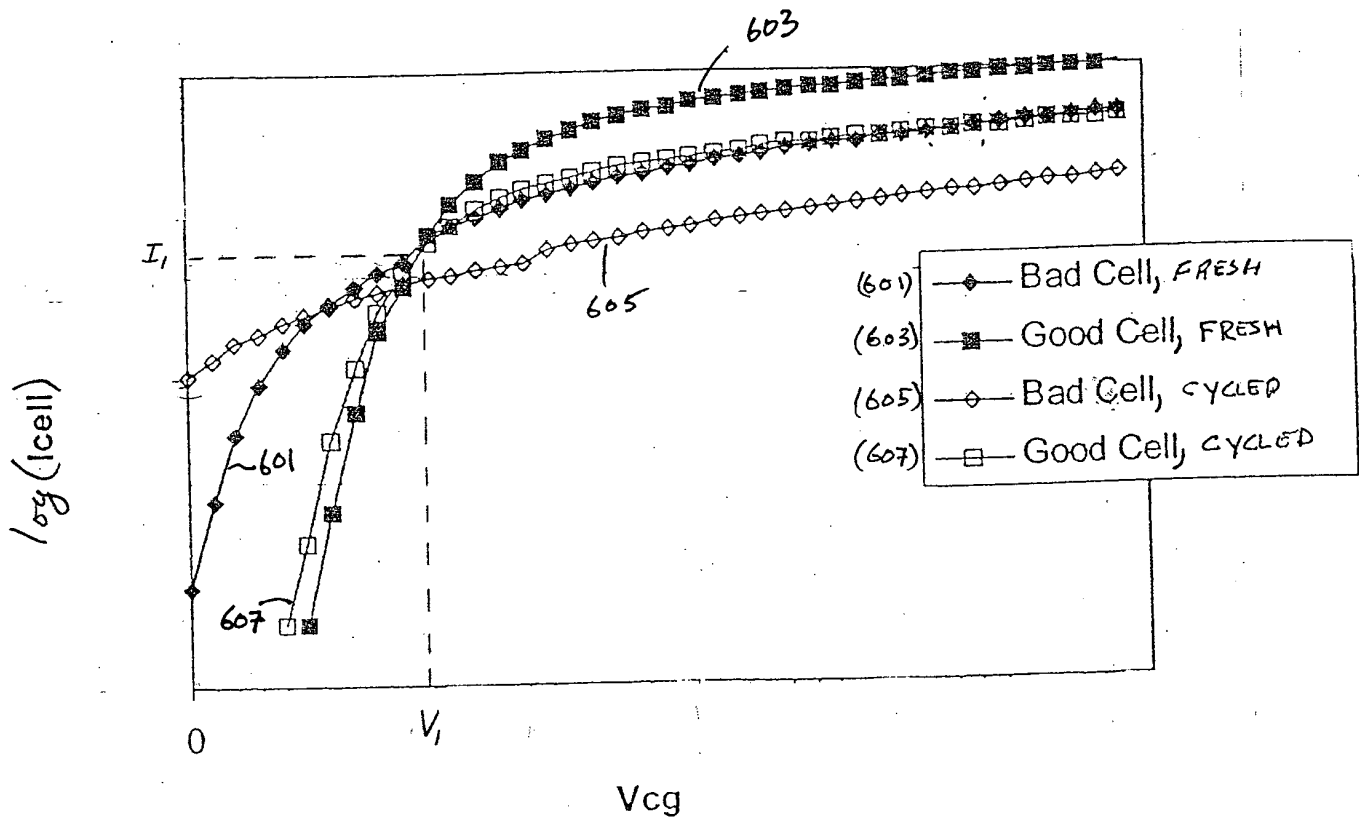


FIGURE 6

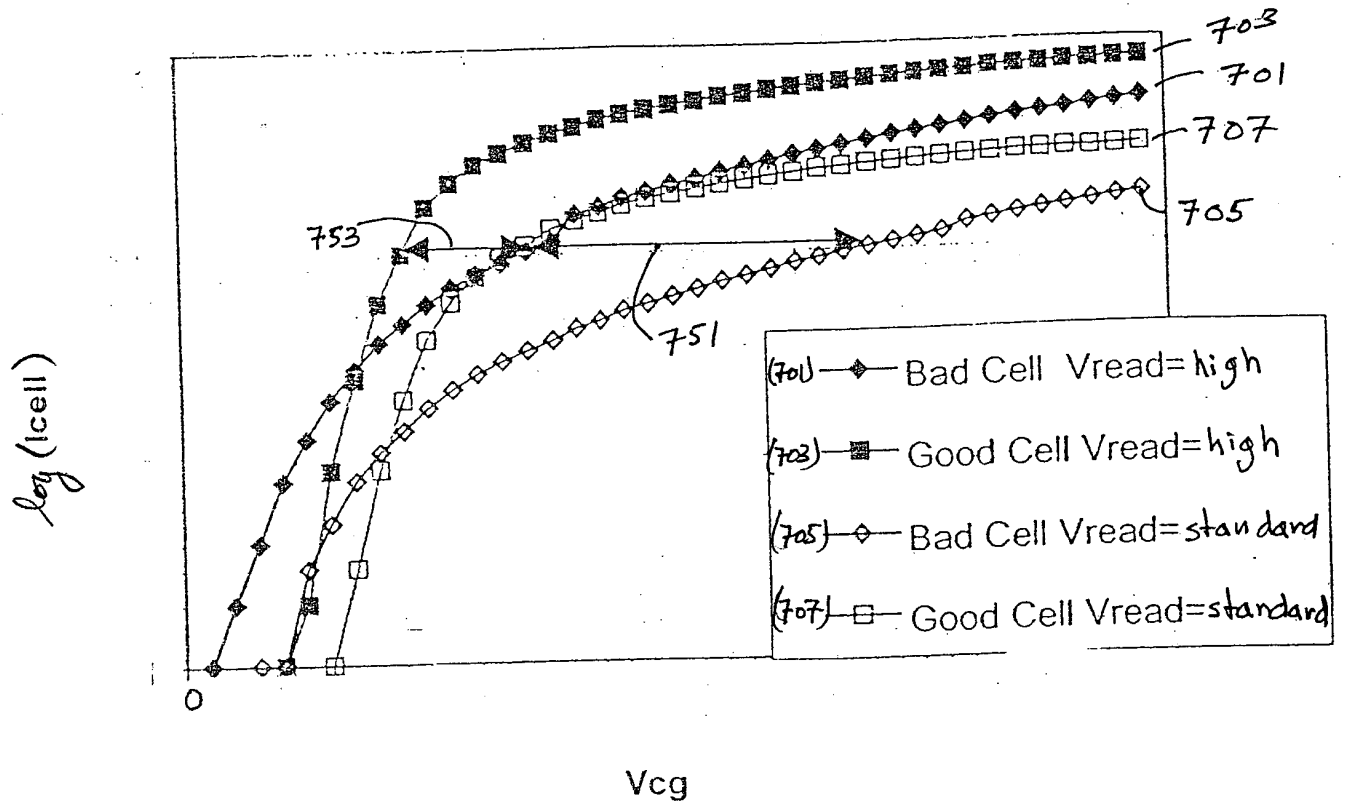


Figure 7

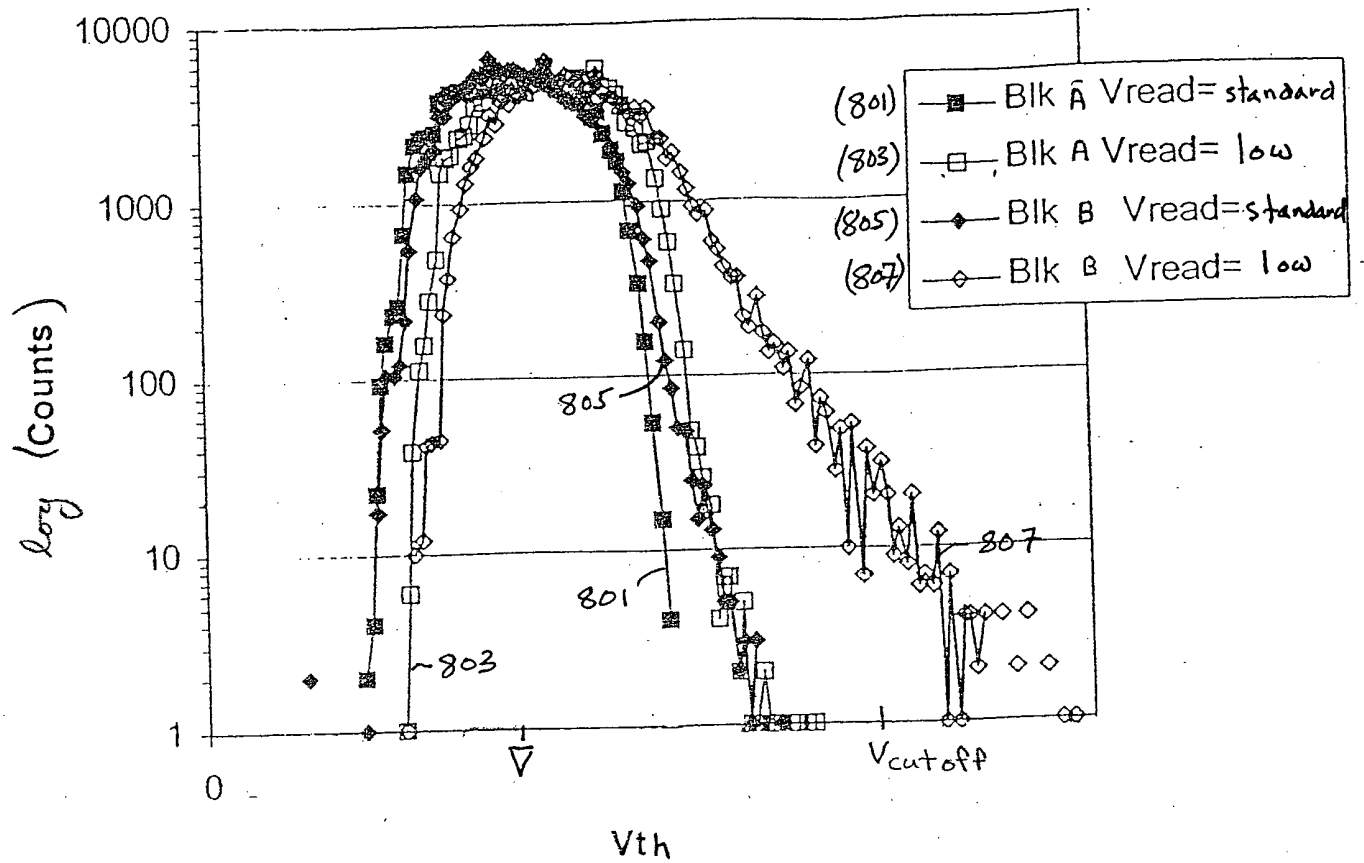


Figure 8